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**IN THE CLAIMS**

Please cancel claims 2 and 14-20 without prejudice or disclaimer as to their subject matter, amend claims 1, 6 and 10 and newly add claims 21-23 as follows:

1 1. (Currently Amended) A flat panel display, comprising:  
2 a gate line, a data line and a power supply line formed on an insulation substrate;  
3 a pixel region defined by the gate line, the data line and the power supply line; and  
4 a pixel comprising a pixel electrode arranged in the pixel region, the pixel electrode  
5 being formed on the same layer as the power supply line, wherein the power supply line is  
6 arranged on a layer different from the gate line or the data line.

1 Claim 2 (Canceled)

1 3. (Original) The flat panel display of claim 1, the power supply line and pixel  
2 electrode both being formed of the same material.

1 4. (Original) The flat panel display of claim 3, the power supply line and pixel  
2 electrode being formed of a material having both a low resistivity and a high reflectivity.

1 5. (Original) The flat panel display of claim 4, the power supply line and the pixel  
2 electrode being formed of a single film of a material being selected from the group consisting

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of Au, Pt, Ni, Cr, a laminated Ni/Al/Ni film, a laminated Ag/ITO film and a laminated Al/ITO film.

6. (Currently Amended) A flat panel display, comprising:  
a thin film transistor comprising source and drain electrodes, formed on an insulation substrate;  
a data line arranged on a same layer as the source and drain electrodes;  
an insulation film formed on the insulation substrate and on the thin film transistor, the insulation film being perforated by first and second contact holes exposing the source and drain electrodes respectively;  
a pixel electrode formed on the insulation film and connected to one of the source and drain electrodes through one of the first and second contact holes; and  
a power supply layer formed on the insulation film and connected to the other one of the source and drain electrodes through the other one of the first and second contact holes,  
wherein the power supply layer is arranged on a layer different from the data line.

7. (Original) The flat panel display of claim 6, the power supply layer and pixel electrode being formed of the same material.

8. (Original) The flat panel display of claim 6, the power supply layer and pixel electrode being formed of a material having both a low resistivity and a high reflectivity.

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1           9. (Original) The flat panel display of claim 7, wherein the pixel electrode and the  
2 power supply layer being formed of a single film of a material selected from the group  
3 consisting of Au, Pt, Ni, Cr, a laminated Ni/Al/Ni film, a laminated Ag/ITO film and a  
4 laminated Al/ITO film.

1           10. (Currently Amended) A flat panel display, comprising:  
2 an insulation substrate divided into a plurality of pixel regions, each of said pixel  
3 regions being defined by a crossing of a gate line and a data line, the insulation substrate  
4 [[and]] comprising a plurality of thin film transistors, each thin film transistor being  
5 arranged in corresponding ones of said plurality of pixel regions;  
6 an insulation film formed on the substrate;  
7 a plurality of pixel electrodes formed on the insulation film and being electrically  
8 connected to corresponding ones of said plurality of thin film transistors in corresponding  
9 ones of said plurality of pixel regions; and  
10 a power supply layer formed on the insulation film such that the power supply layer  
11 is electrically separated from the plurality of pixel electrodes, said power supply layer being  
12 electrically connected to each of the plurality of thin film transistors and supplying power  
13 to each of the plurality of thin film transistors, wherein the power supply layer is arranged  
14 on a layer different from the gate line or the data line.

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1 11. (Original) The flat panel display of claim 10, the power supply layer being  
2 formed in a grid shape in which corresponding ones of said plurality of pixel electrodes being  
3 disposed in each grid.

1 12. (Original) The flat panel display of claim 10, the power supply layer being  
2 formed in a line shape in which the power supply layer is arranged between corresponding  
3 ones of said plurality of pixel electrodes, said power supply layer being arranged in one of  
4 a row or a column.

1 13. (Original) The flat panel display of claim 10, the power supply layer having a  
2 surface electrode shape in which the power supply layer is formed on a whole surface of the  
3 substrate and being electrically separated from each of the plurality of pixel electrodes.

1 Claims 14-20 (Canceled)

1 21. (New) The flat panel display of claim 1, the power supply line having a grid shape  
2 and surrounding the pixel region.

1 22. (New) The flat panel display of claim 6, the power supply layer having a grid  
2 shape and surrounding a pixel region.

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1           23. (New) The flat panel display of claim 10, wherein the power supply layer  
2           surrounds each of said plurality of pixel electrodes, the power supply layer comprises a  
3           plurality of electrodes extending in a first direction and a plurality of electrodes extending  
4           in a second direction intersecting the electrodes extending in the first direction.